

| Ref # | Hits | Search Query    | DBs                | Default Operator | Plurals | Time Stamp       |
|-------|------|-----------------|--------------------|------------------|---------|------------------|
| L1    | 50   | (257/548).CCLS. | US-PGPUB;<br>USPAT | OR               | OFF     | 2005/01/24 15:30 |
| L2    | 148  | (257/547).CCLS. | US-PGPUB;<br>USPAT | OR               | OFF     | 2005/01/24 15:30 |

| Ref # | Hits | Search Query   | DBs             | Default Operator | Plurals | Time Stamp       |
|-------|------|--|-----------------|------------------|---------|------------------|
| L1    | 13   | latch-up and dmos and isolation and motor and semiconductor  | US-PGPUB; USPAT | OR               | OFF     | 2005/01/24 14:00 |
| L2    | 29   | latch-up and dmos and isolation and ground and semiconductor | US-PGPUB; USPAT | OR               | OFF     | 2005/01/24 14:17 |
| L3    | 87   | 257/341.ccls. and latch                                      | US-PGPUB; USPAT | OR               | OFF     | 2005/01/24 14:34 |
| L4    | 45   | motor adj control and dmos and integrated                    | US-PGPUB; USPAT | OR               | OFF     | 2005/01/24 14:49 |